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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kiran Ganesh et al.
Title: 2-DIMENSIONAL PLACEMENT WITH RELIABILITY CONSTRAINTS FOR VLSI DESIGN
Docket No.: 884.141US1
Filed: November 1, 1999
Examiner: Phallaka Kik
Serial No.: 09/431,477
Due Date: December 26, 2002
Group Art Unit: 2825

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- ☒ A return postcard.
- ☒ A Notice of Appeal (1 Page).
- ☒ Petition for Extension of Time (1 pg.)

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